

# Xiaoji Yang

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## Education

Exp. 09/2002	<b>Ph.D</b>	<b>Northeastern University, Boston, MA</b> Major: Electrical and Computer Engineering Area: Electronic Circuits, Solid State Devices and Microfabrication	<b>(GPA 3.7/4.0)</b>
01/1996	<b>M.S</b>	<b>Tsinghua University, Beijing, China</b> Major: Electronic Engineering Area: Physical Electronics and Optoelectronics	<b>(GPA 3.8/4.0)</b>
07/1993	<b>B.S</b>	<b>Tsinghua University, Beijing, China</b> Major: Electronic Engineering Area: Physical Electronics and Optoelectronics	<b>(GPA 3.8/4.0)</b>

## Experiences

1999 to Present	<b>Northeastern University, Boston, MA</b> <b>Research Assistant</b>
	<ul style="list-style-type: none"><li>➤ Research on <b>plasma etching</b> of Cesium iodide (CsI) for X-ray imaging sensors</li><li>➤ Research on <b>plasma diagnostics</b>, such as optical emission spectroscopy and mass spectroscopy</li><li>➤ Measuring wafer temperature in plasma with interferometry method</li><li>➤ Construct a model to explain the mechanism of plasma etching of CsI (The project is sponsored by NIH and RMD inc.)</li></ul>
1993 to 1999	<b>Tsinghua University, Beijing, China</b> <b>Lecturer</b> (1996 to 1999) <b>Research Assistant</b> (1993 to 1996)
	<ul style="list-style-type: none"><li>➤ Conducted research on composite <b>thin film of Low-e coatings</b> (Energy saving coatings) (The program is sponsored by the National "9·5" Plan)</li><li>➤ Conducted research on the <b>spectral selective films</b> for solar energy conversion (The program is sponsored by the National "8·5" Plan)</li><li>➤ Designed a circuit for capacitance manometer for measuring vacuum</li><li>➤ Taught undergraduate student course "Thin Film Physics and Technology".</li></ul>

## Technical Skills

- Strong background in **plasma etching, deposition, diagnostics and vacuum technologies**
- In-depth knowledge on solid state device physics
- Familiar with integrated circuit (IC) fabrication procedures

## Computer Skill & CAD Tools

- Proficient with **UNIX, Windows/Windows NT, DOS** platforms
- **Computer languages:** C/C++, FORTRAN, VB
- **CAD Tools:** MATLAB, VHDL, Hspice, Synopsys, TSUPREM

## Major Courses

Solid State Devices (I, II)  
High Speed/High Frequency Solid State Devices (III-V Semiconductors)  
**Integrated Circuits** Fabrication (I, II), **Plasma Processing**  
Microelectromechanical Systems (**MEMS**)  
**VLSI Design**  
Digital System Design with Hardware Description Language (**VHDL**)  
**Digital Hardware Synthesis**  
**Electronic Circuit** for Analog Signal Processing  
Electromagnetic Theory (I,II)