JOHN KALAMATIANOS
44 College Ave., Arlington MA 02474
Phone : (781) 641-5377 (h) - (617) 373-7780 (w)
e-mail: kalamat@ece.neu.edu

Objective: A position as a Computer/Compiler Architect and/or VLSI designer

Skills: *Programming:* C, C++, Fortran, VHDL, Pascal, Matlab, PL/I
Operating Systems: Solaris, Digital Unix, AIX, Windows NT4.0

Education:

Northeastern University, Boston, MA — *expected in 1999*
Ph.D. in Electrical & Computer Engineering

Northeastern University, Boston, MA — *October 1995*
M.Sc. in Electrical & Computer Engineering, GPA : 4.0/4.0

University of Patras, Patras, Greece — *November 1992*
B.S. in Computer Engineering & Science, GPA : 8.67/10.0

Experience:

Microsoft Research (Redmond, WA) — *06/1998 - 08/1998*
Summer Intern: Traced and measured the temporal locality and invariance of parameter values for applications running under Windows NT4.0

IBM Watson Res. Center (Yorktown Hgts, NY) — *Jun 1996 - Sep 1996*
Summer Intern: Ported a cycle-accurate simulator to IBM AIX and performed cycle-based simulations for the Alliance CPU used in the IBM AS/400 and S/390 computer systems

Northeastern University (Boston, MA) — *Oct 1995 - present*
Research Assistant: Conducted research in the areas of code reordering and indirect branch prediction for High Performance Microprocessors

Northeastern University (Boston, MA) — *Sep 1993 - Oct 1995*
Research Assistant: Conducted research in the area of SIMD parallelizing compilers, developed VHDL models for a Column-Associative Cache and a Scalable 1-D DWT architecture

Computer Technology Institute (Patras, Greece) — *May 1993 - Jul 1993*
Testing Engineer: Tested board-level prototypes of an Educational Microprogrammable Computer system designed in my Diploma thesis

Computer Technology Institute (Patras, Greece) — *Dec 1990 - Jan 1992*
Teaching Assistant: Assisted and organized the Microprocessor I, II and CAD tools Labs

University of Patras (Patras, Greece) — *Sep 1992 - Jul 1993*
Research Assistant: Conducted research in the area of CMOS VLSI modulo $2^N - 1$ adders, organized and supervised the Computer Architecture Lab