

ECEU322

Digital Logic Design

Handout 3

Fall 2005

October 7, 2005

Midterm Information

The first midterm exam will be Thursday, October 13 in class.

The test will be closed book and closed notes. You may not use a calculator or any other electronic devices.

The material covered will include: You are expected to know conversion between different number systems, Binary, Octal, and Hexadecimal notation, and different representations of signed numbers (sign magnitude, one's complement, 2's complement), logic gates, Boolean algebra, truth tables, canonical forms, analysis of combinational circuits, design of combinational circuits, conversion to all NANDS or all NORs, minimization to SOP or POS form through K-maps.

This material is covered in

Lecture Notes Lectures 1 to 11 (excluding modular logic).

Chapter 1 Sections 1.1, 1.1.1, 1.1.3, 1.2, 1.3, and 1.4.

Chapter 2 All sections.

Chapter 3 3-3.3.

You are expected to know the material covered in **Homeworks** 1 through 3 and **Labs** 1 and 2.

There will be a review session in class on Wednesday, Oct. 12. I will offer an extra office hour on Wednesday from 11:35 am to 12:35 pm.

The book offers several solved problems for practice.

There is a practice exam on the course web page under Handouts. We will go through this exam in class on Wednesday. It is recommended that you do the exam yourself before coming to class.