# EECE 2150 – Circuits and Signals Final Exam – Fall 2016 – Dec 12

## Name:

## Section:

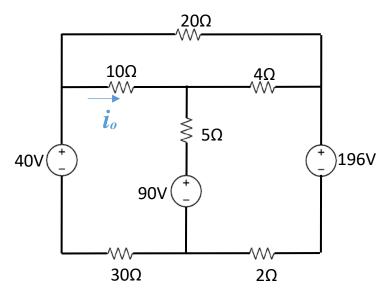
#### **Instructions:**

- Write your name and section number on all pages
- Closed book, closed notes; Computers and cell phones are not allowed
- You can use a single, double-sided, equation sheet
- Scientific calculators are allowed
- Complete 5 problems, if you start to work on more than 5 problems be sure to make clear which 5 problems you want graded. Otherwise the first 5 problems which you started answering will be graded.
- All problems will have an equal value of 20%.
- Show all work and place a box around all your final answers
- Show your work clearly and in detail for partial credit
- You may write on both sides of the pages

### Section:

### Problem 1)

Use Node-Voltage or Mesh-Current method and calculate current  $i_o$  in the circuit.

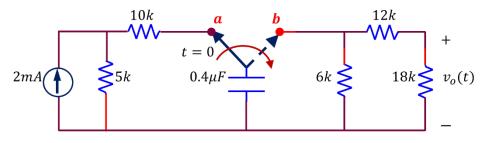


#### Name:

### Problem 2)

For the circuit shown, the capacitor stays in position (a) for a long time before it is switched to position (b) at t = 0

- a. Find the initial voltage across the capacitor at t = 0
- b. Find the time constant of the circuit after switching to position (b)
- c. Write a complete expression for the voltage across the capacitor as a function of time,  $t \ge 0$
- d. Write a complete expression for the output voltage,  $v_o(t)$  as a function of time,  $t \ge 0$



#### Name:

### Problem 3)

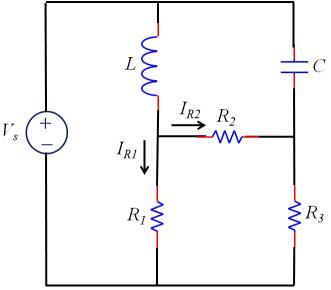
For the circuit of the figure below,

- a. Find the current going through  $R_2$  for  $V_s = 10$  volts. Explain your answer using the behavior of the capacitor and inductor for constant (DC) sources.
- b. Find the current going through  $R_2$  for  $V_s = 15\cos(10^{10}t + \frac{\pi}{3})$  volts. (You can assume that this frequency is high enough that you can let  $\omega \rightarrow \infty$ ). Explain your answer using the behavior of the capacitor and inductor at very high frequencies.
- c. Using sinusoidal steady state analysis, find the current going through  $R_1$  for  $V_s = 3\cos(10^3 t + \frac{\pi}{4})$  volts.

$$R_1=10\Omega$$
,  $R_2=20\Omega$ 

 $R_2 = 10\Omega$ 

L = 10mH  $C = 100\mu F$ 



### Problem 4)

The parts of this problem are independent. You can use the tables of Fourier Transform pairs and properties anywhere they are useful as long as you state which property or pair you are using.

a. Find the Fourier Transform of a signal x(t) if

$$x(t) = \begin{cases} 1.5 & 2 < t < 4 \\ 0 & otherwise \end{cases}$$

b. Find the Fourier Transform of a signal y(t) if

$$y(t) = \begin{cases} -3e^{-4t} & t > 1\\ 0 & t \le 1 \end{cases}$$

Note that if the notation for the step function is familiar to you, you may find it helpful that this is the same as saying that  $y(t) = -3e^{-4t}u(t-1)$ 

c. If a system with input x(t) and output y(t) is described by the differential equation

$$y''(t) + 5y'(t) - y(t) = -x'(t) + 2x(t),$$

give an expression for the frequency response of the system  $Y(\omega) / X(\omega)$ .

d. A linear time invariant system has input x(t) and output y(t). If the Fourier Transform of the input is  $X(\omega) = \frac{2}{2-j3\omega}$  and the frequency response of the system is  $H(\omega) = \frac{j2\omega}{3+j4\omega}$ , give an expression for Y( $\omega$ ), the Fourier Transform of y(t). Note that you do not need to evaluate this Fourier Transform in any way, just tell us what it is.

### Problem 5)

For a particular active (Op-Amp) filter, the frequency response is:

$$H(\boldsymbol{\omega}) = \frac{V_o(\boldsymbol{\omega})}{V_s(\boldsymbol{\omega})} = \frac{\frac{j\omega}{R_1C}}{(j\omega)^2 + j\omega\left(\frac{2}{R_3C}\right) + (\frac{1}{R_2R_3C^2})},$$

a. Based on this expression, what is the value of this frequency response when the frequency  $\omega = 0$ ? Show your work to receive full credit!

b. Based on this expression, what is the value of this frequency response when the frequency  $\omega \to \infty$ ? Show your work to receive full credit.

c. Based on your answers to the questions above, what kind of filter is this? Lowpass, highpass, bandpass, band reject or none of those? Explain your reasoning to receive full credit.

d. If the element values for the circuit are chosen such that  $R_1C = 1$ ,  $R_2C = 0.5$  and  $R_3C = 0.5$ . what is the output of this filter,  $v_0(t)$ , if  $v_s(t) = 4 + 2\cos(2t + \pi/3) + 4\cos(1000t + \pi/4)$ ? Note that if, in carrying out this computation, you come across a complex number where the real and imaginary parts differ by more than 2 orders of magnitude (in

where the real and imaginary parts differ by more than 2 orders of magnitude (in other words by more than a factor of 100) you can approximate by neglecting the smaller term.

#### Name:

### Problem 6)

The trigonometric Fourier series for a square wave at  $\omega 0=100\pi$  rad/s oscillating from 0 to 1 is given by

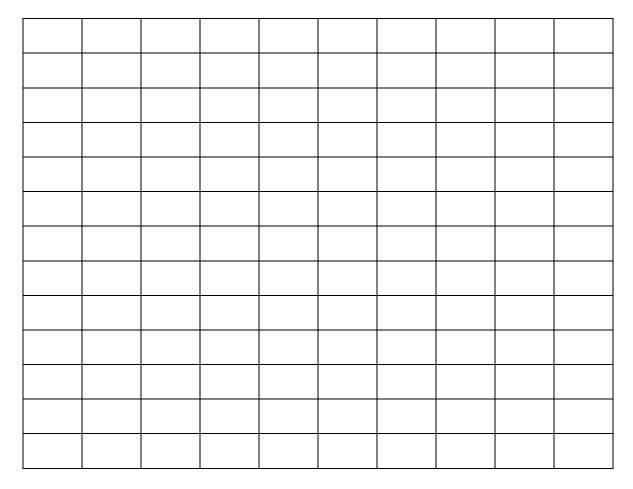
$$f(t) = a_0 + \sum_{n=1}^{\infty} a_n \cos n\omega_0 t + b_n \sin n\omega_0 t$$

where a0=0.5, and otherwise:

$$a_n = (-1)^{\frac{n+3}{2}} \frac{2}{n\pi}$$
, n odd; 0, n even

and bn=0.

- a. Plot the sum of a0 and a1.
- b. Plot the sum of a0, a1, and a2.



#### Name:

c. Plot the output if you put the signal (the original square wave) into an ideal high-pass filter with a cutoff (or cut-on) frequency  $\omega c=50\pi$  rad/sec, and an in-band gain of 1. Explain your reasoning.

 1	1	1	1		1	

### Problem 7)

You have an audio signal from a microphone that contains frequency components from 20Hz to 60kHz. The voltage of the signal ranges from 0 to  $\pm 100$  mV as a function of time. You plan to sample the signal at 50kHz using a 10-bit A/D (analog to digital) converter with an input range of -10 to 10 V.

a. To preserve all frequencies over the human hearing range (20Hz to 20kHz) while minimizing aliasing (distortion) of your sampled signal, you need to filter the signal before the A/D conversion. Approximately what should the cutoff frequency of the filter be?

b. How many voltage levels are output by a 10-bit A/D? How large is each voltage level for this A/D?

c. Now, instead of the audio signal, assume that the input to your A/D is  $0.05\cos(10000\pi t)$  volts. Sketch the digital output below for 1 cycle, including quantization effects (quantization is the mapping of the analog signal onto a discrete digital value at each sampling time).

d. To minimize quantization effects, your signal should be amplified before A/D conversion. What should the gain of your amplifier be to minimize quantization effects?